

Form PTO 1449 (Modified)		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO.: 8733.337.00		SERIAL NO.: 09/731,895	
LIST OF REFERENCES CITED BY APPLICANT (Use Several Sheets if necessary)				APPLICANT: Hyeon-Ho SON			
				FILING DATE: December 8, 2001		GROUP: 2871	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES NO		
	AC						
	AD						
OTHER REFERENCES (Including Author, Title, Date, Pertinent Pages, Etc.)							
DN	AE	N. Koma, Y. Baba, K. Matsuoka, No-Rub Multi-Domain TFT-LCD Using Surrounding Electrode Method, SID Digest, 1995, pgs. 869-872					
DN	AF	Y. Tanaka et al., Late News Paper: A New Design to Improve Performance and Simplify the Manufacturing Process of High Quality MVA TFT-LCD Panels, Sid Digest 99, pp. 206-207					
	AG						
	AH						
	AI						
	AJ						
	AK						
EXAMINER:	DNAMEN				DATE CONSIDERED:	07/12/03	
*EXAMINER: Initial if reference is considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							